

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Michael K. Gschwind, Martin E. Hopkins,  
H. Peter Hofstee

Serial No.: Unassigned

Filed: Concurrently herewith

For: **METHOD AND APPARATUS FOR ALIGNING MEMORY WRITE  
DATA IN A MICROPROCESSOR**



Assistant Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to Applicant(s) duty of disclosure, the information listed in the attached form PTO-1449 is brought to the attention of the Examiner. Copies of the listed items are enclosed.

The citation of the listed items is not a representation that they constitute a complete or exhaustive listing of the relevant art or that the references are prior art. The items listed are submitted in good faith, but are not intended to substitute for the Examiner's search. It is hoped,

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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Assistant Commissioner for Patents, Washington, D.C. 20231 on August 28, 2001.

Dated: Aug. 28, 2001

  
Gaspare J. Randazzo

however, that in addition to apprising the Examiner of these particular items, they will assist in identifying fields of search and in making as full and complete a search as possible.

The filing of this information disclosure statement is not an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

This information disclosure statement is being filed within three (3) months of the filing date of this application.

This information disclosure statement is being filed within three (3) months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application.

To the best of Applicant(s) knowledge, this information disclosure statement is being filed before the date of mailing of a first Office Action on the merits in connection with this case.

Statement under 37 C.F.R. § 1.97(e) :

I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than 3 months prior to the filing of this statement.

[ ] Enclosed herewith is a petition under 37 C.F.R. § 1.97(d)(ii).

[ ] Enclosed by check is the petition fee of \$130.00. (37 C.F.R. § 1.17(i)(1))

[ ] Please charge the \$130.00 petition fee to Deposit Account No. 50-0510/IBM (Yorktown Heights).

[ ] Enclosed by check is the \$240.00 fee required by 37 C.F.R. § 1.17(p).

[ ] Please charge the \$240.00 fee required by 37 C.F.R. § 1.17(p) to Deposit Account No. 50-0510/IBM.

[x] Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. § 1.16

and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account 50-0510/IBM. Also, in the event any extensions of time for responding are required for the pending application(s), please treat this paper as a petition to extend the time as required and charge Deposit Account No. 50-0510/IBM therefor. **TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.**

The claims of the application as now presented are believed to patentably distinguish over the prior art and to be in condition for allowance. Early and favorable consideration of the case is respectfully requested.

Respectfully submitted,

By:

  
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Form PTO-1449 OF COMMERCE OFFICE	U.S. DEPARTMENT PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. Y0R9-2001-0606 (8728-545)	SERIAL NO. Unassigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)		 09/28/01	
		APPLICANT Michael K. Gschwind, Martin E. Hopkins, H. Peter Hofstee	
		FILING DATE Concurrently herewith	GROUP Art Unit

## U.S. PATENT DOCUMENT

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4 8 1 4 9 7 6	3/21/1989	Hansen et al.			

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES
							NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		1. John L. Hennessy, "VLSI Processor Architecture," December 1984, IEEE Transactions On Computers, Vol. C-33, No. 12, pp. 1221-1246
		2. Richard L. Sites, "Alpha AXP Architecture," February 1993, Communications of the ACM, Vol. 36., No. 2, pp. 33 - 44
		3. Patent Application entitled "Processor Implementation having Unified Scalar and SIMD Datapath." Attorney Docket No.: Y0R9-2001-0675, filed on August 14, 2001

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.